KP1100A4300~5500V
Y65KPM

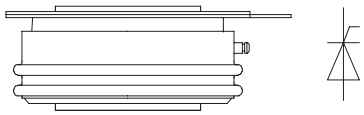
国标型-普通晶闸管(平板式)

Chinese Type Phase Control Thyristors (Capsule Version)

Liujing rectifier co., Ltd.

FEATURES

1). Center amplifying gate
2). Metal case with ceramic insulator
3). Low on-state and switching losses



TYPICAL APPLICATIONS

1). AC controllers
2). DC and AC motor control
3). Controlled rectifiers

$I_{T(AV)}$	1300A
V_{DRM}/V_{RRM}	4300~5500V
I_{TSM}	17 KA
I^2t	1445 10^3A^2S

THE MAIN PARAMETERS

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T_j(℃)	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180°half sine wave 50Hz Double side cooled, T_{hs}=55℃	125			1300	A
		T_{hs}=71℃				1100	
V_{DRM} V_{RRM}	Repetitive peak off-state voltage Repetitive peak reverse voltage	V_{DRM}&V_{RRM},tp=10ms V_{DSM}&V_{RSM}= V_{DRM}&V_{RRM}+100V	125	4300		5500	V
I_{DRM} I_{RRM}	Repetitive peak off-state current Repetitive peak reverse current	V_D= V_{DRM} V_R= V_{RRM}	125			120	mA
I_{TSM}	Surge on-state current	10ms half sine wave V_R=0.6V_{RRM}	125			17	KA
I^2t	I^2T for fusing coordination					1445	A^2s*10^3
V_{TO}	Threshold voltage		125			1.30	V
r_T	On-state slop resistance					0.58	mΩ
V_{TM}	Peak on-state voltage	I_{TM}=3220A, F=32KN	125			3.17	V
dv/dt	Critical rate of rise of off-state voltage	V_{DM}=0.67V_{DRM}	125			1000	V/μs
di/dt	Critical rate of rise of on-state current	V_{DM}= 67%V_{DRM} to2000A, Gate pulse t_r ≤0.5 μs I_{GM}=1.5A	125			200	A/μs
I_{rm}	Reverse recovery current	I_{TM}=1000A,tp=1000 μs, di/dt=-20A/ μs, VR=50V	125			250	A
t_{rr}	Reverse recovery time					21	μs
Q_{rr}	Recovery charge					2100	μC
I_{GT}	Gate trigger current	V_A=12V, I_A=1A	25	40		300	mA
V_{GT}	Gate trigger voltage			0.8		3.0	V
I_H	Holding current			20		250	mA
V_{GD}	Non-trigger gate voltage	V_{DM}=67%V_{DRM}	125	0.3			V
$R_{th(j-h)}$	Thermal resistance Junction to heatsink	At 180° sine, double side cooled Clamping force 32KN				0.017	℃ /W
F_m	Mounting force			27		34	KN
T_{stg}	Stored temperature			-40		140	℃
W_t	Weight				650		g
Size	Package box size	160 × 145 × 65					mm

PERFORMANCE CURVES FIGURE

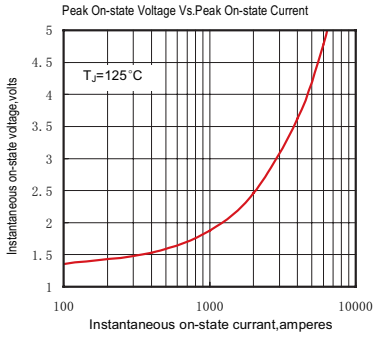


Fig.1

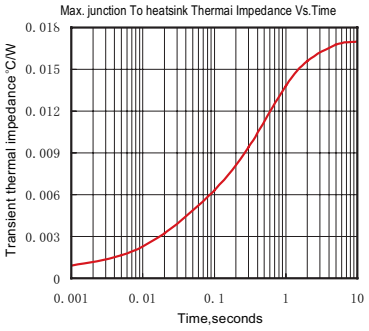


Fig.2

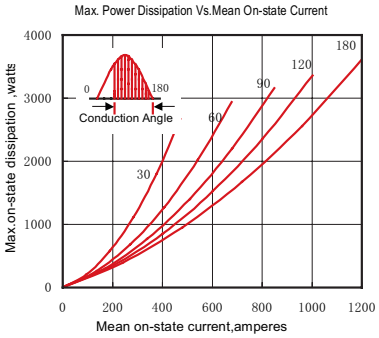


Fig.3

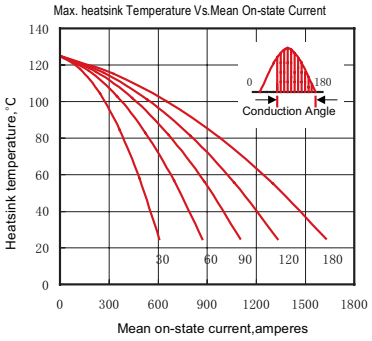


Fig.4

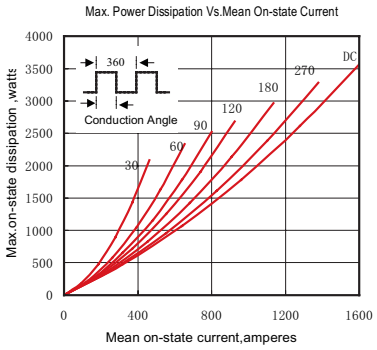


Fig.5

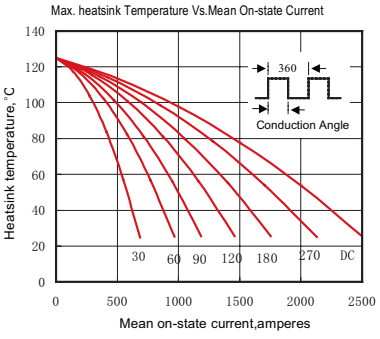


Fig.6

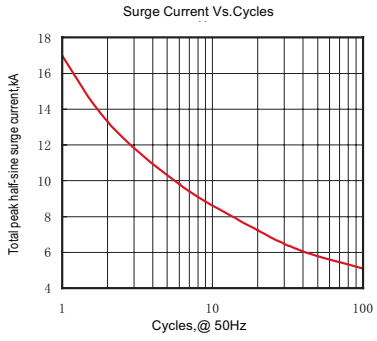


Fig.7

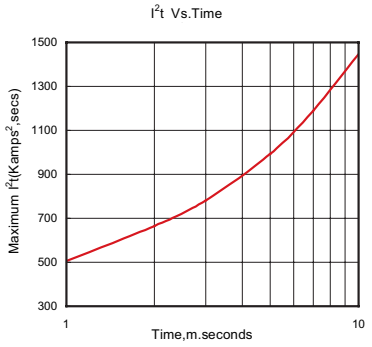


Fig.8

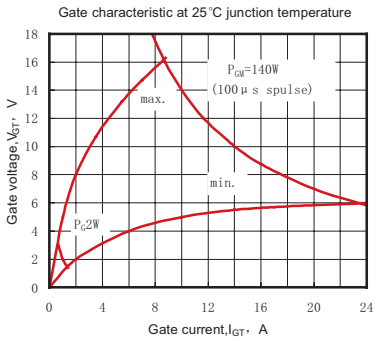


Fig.9

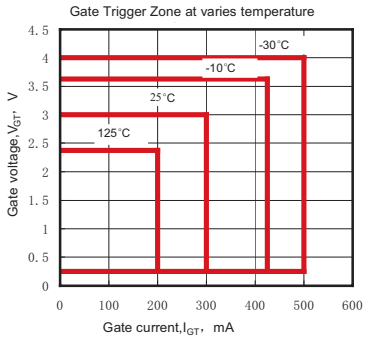
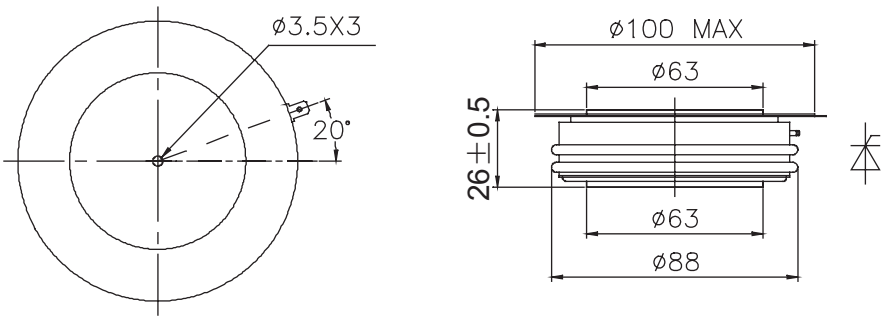


Fig.10

OUTLINE



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